

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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ors:

Michael C. Dorsey

Serial No .:

09/976,523

Filing Date:

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For: Utilizing Slow ASIC Logic BIST to

Preserve Timing Integrity Across Timing Domains Atty. Dkt. No.: 5681-56300

Art Unit: 2858

Examiner: Unknown

I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231, on the date indicated below:

B. Noel Kivlin

Name of Registered Representative

Date Date

Date Signature

NOTICE OF CHANGE OF ADDRESS

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Commissioner for Patents Washington, D.C. 20231

Sir:

Applicant respectfully requests the Commissioner to change the correspondence address for the above identified patent application. The old correspondence address was:

Williams Morgan & Amerson PC 7676 Hillmont Suite 250 Houston, TX 77040

The new correspondence address is:

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TECHNOLOGY CENTER 2800

If there are any questions regarding this matter, please contact me at the telephone number provided below.

Respectfully submitted,

B. Noël Kivlin

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Date: 4-15-10